

Electronic Design and Solution Fair 2007

Early Application Deadline	September 8, 2006(Fri.)
Secondary Deadline	September 29, 2006(Fri.)
Final Deadline	November 10, 2006(Fri.)

Emerging Company Area- Application Form

We agree to conform to all the provisions in the Guide for Exhibitors and hereby apply to exhibit at the Electronic Design and Solution Fair 2007 as follows:
 Company names, titles of people in charge, contact information and other details provided at the time of exhibitor application will be compiled in a directory by the EDSFair 2007 Office. We will use this information to contact exhibitors and send relevant documents (invoices, etc.) related to the event's operation. The EDSFair 2007 Office will closely guard all personal information, which will not be disclosed to third parties without prior consent. For more information about our privacy policy, please contact the Japan Electronics Show Association (JESA).

■ Applicant Information

This information will appear in printed materials and on the official EDSFair website exactly as written, so please be careful to provide precise information.
 Name of Company(One letter per box, leave box empty for space)

Name of Company																					
Homepage URL																					
Head Office Address																					
CEO's Name																			CEO's Official Title		
Name of Person in Charge of Exhibit																			Title / Division		
Address of Person in Charge of Exhibition																					
Phone																			F A X		
E-Mail																					

*If you have not exhibited at EDSFair before, you are requested to submit your company profile and product catalogs.

■ Applicable Products

Check one category.

Exhibition Category	
<input type="checkbox"/> 1. Hardware Solutions	<input type="checkbox"/> 7. Design service-related
<input type="checkbox"/> 2. Hardware Development (EDA)	<input type="checkbox"/> 8. Design infrastructure (WS/PC, Network)
<input type="checkbox"/> 3. Software solutions	<input type="checkbox"/> 9. Design data management tool
<input type="checkbox"/> 4. IC tester/analyzer	<input type="checkbox"/> 10. Mask shop, Foundry
<input type="checkbox"/> 5. IP core, macro, cell libraries	<input type="checkbox"/> 11. University (R&D), Consortium
<input type="checkbox"/> 6. Embedded processor development environments	

■ How to Apply

Fill in the appropriate application form(s) and submit to JESA by post.
 JESA will accept form submissions by fax. In such cases, the date of receipt will be the day it faxed. However, exhibitors are also required to send the original form(s) by post.

Send to Japan Electronics Show Association
 Attn., Ms. Yuka Arikawa / Mr. Fumiaki Yoshinaga
 5F, Sumitomo Shibadaimon Bldg.
 1-12-16, Shibadaimon, Minato-ku, Tokyo, 105-0012, Japan
 Phone: +81-3-5402-7601 Fax: +81-3-5402-7605

Note: If changes arise for application, notify JESA Management office via fax or e-mail as soon as possible.

For Management Office Use

Early application	Secondary application	Reception#	Member	Input	Invoicing	Booth No.