

Dassault Systemes (ENOVIA)



- Company Location
 - Headquarter: Suresnes Cedex, France
 - Japan Office: Pier City Shibaura Bldg. 9F. 3-18-1 Kaigan, Minato-ku, Tokyo
- Company Profile
 - As a world leader in 3D and Product Life Cycle Management (PLM) solutions, Dassault Systemes develops and markets PLM application software and services that support industrial processes and provide a 3D vision of the entire lifecycle of products from conception to maintenance. ENOVIA, a brand of Dassault Systemes, provides global collaborative lifecycle management, including ENOVIA VPLM, ENOVIA MatrixOne and ENOVIA SmarTeam.
- Product Names
 - **ENOVIA MatrixOne - ENOVIA Synchronicity DesignSync®**
 - ENOVIA SmarTeam
 - ENOVIA VPLM

Dassault Systemes (ENOVIA)

- Product Key Features
 - ENOVIA MatrixOne
 - Provide collaborative business processes for the enterprise level organization
 - **ENOVIA Synchronicity DesignSync®**
 - *DesignSync & HCM Data Repository with EDA Data Awareness™*
- Key Benefits
 - EDA Data-Aware Design Data Management Enabling collaboration
 - Reduced Design Costs and Cycle Time and Improved Competitiveness

Micrologic Design Automation



- **Location**
 - **USA**
 - West Hollywood, California – Headquarters
 - **Israel**
 - Haifa – R&D, Marketing, Support
- **Profile**
 - We are a leading developer of semiconductor design EDA software, enabling integrated circuit corporations to meet or even shorten their time-to-market objectives and improve their nanometer design quality, reliability and performance, significantly enhancing multimillion-gate chip design productivity!
- **Products**
 - **nanoRVIInteractive™ - Reliability-Aware Design Environment**
 - **VisuaLVS™ - Automatic LVS Correction System**

Micrologic Design Automation



- **Product's Key Features**

- **nanoRVInteractive™** -

- Reliability-Aware Design Environment

- **VisuaLVS™** -

- Automatic LVS Correction System

- **Product's Key Benefits**

- **nanoRVInteractive™** -

- Interactive Reliability Verification during IC layout construction! Major sign-off verification time savings!

- **VisuaLVS™** -

- Significantly reduces LVS Verification time, improving layout quality and performance!

SoftJin Technologies



- Company Location

- Bangalore (India)

- Company Profile

SoftJin is a leading provider of

- Custom EDA software development services
 - Post Layout EDA products
 - FPGA based Hardware design services

- Product Names

- Nirmaan – Toolkit for Post Layout EDA and DFM tool development
 - NxCompare – Layout and Mask Data Equivalence Checker
 - Tools for GDSII, OASIS, MEBES, other Mask/Layout formats

SoftJin Technologies

- Product Key Features
 - Nirmaan
 - Software toolkit for development of post-layout EDA tools including DFM tools
 - Includes in-built geometrical operations and database
 - Benefits
 - Develop any Post Layout EDA tool of your choice
 - NxCompare
 - Fast, Cross-Format Geometrical Equivalence Tool
 - Benefits
 - Real fast geometrical comparison using distributed computing, better than conventional DRC tools
 - Unlimited data handling capacity (handle 100's of GBs)

Sidense Corp.



- Company Location
 - Ottawa, Canada
- Company Profile
 - 標準CMOSロジック (180nm, 130nm, 90nm, 65nm and later) 対応のフィールド書込み型、OTP/eMTP向け、不揮発性メモリーIPコア ライセンスビジネス。
- Product Names
 - 1T-Fuse (patented architecture)
 - SiFUSE IP Core (<64Kbits, hard macro)
 - SiPROM IP Core (64Kbit<, hard macro)

Sidense Corp.



- Product Key Features と用途
 - SiFUSE/SiPROM Core
 - 128bits から 8Mbitsまでのビット対応
 - Built-in charge pump
 - HDMI, DRM, Chip ID, トリミング、暗号キーなど向け
 - フューズ、マスクROMなどストレージ代替など
- Key Benefits
 - >20 years of data retention and reliability
 - Extended operating temperature -40C to +125C
 - マスクROMへのマイグレーションなど

Terasic Technologies, Inc.

- **Company Location**
 - HsinChu, Taiwan
- **Company profile**
 - Terasic, founded in 2003 with \$7M USD received funding, provides state-of-the-art systems using Altera FPGA
- **Product names**
 - Nios II Embedded Evaluation kit (NEEK)
 - HSMC Daughter Boards
 - TREX-S2

Terasic Technologies, Inc.



- Product Key Features
 - Nios II Embedded Evaluation kit (NEEK)
 - Upgrading the CycloneIII starter board
 - HSMC Daughter boards
 - Supporting Altera HSMC Bus
 - TRES-S2
 - Almost 700 available user I/O with StratixII 60, 180
- Key Benefits
 - As a leader of EMS company,
We provide very high quality products