



# Azuro, Inc.

- Company Location
  - Santa Clara, California, USA
- Company Profile
  - A provider of advanced clock implementation tools for nanometer chip design
- Product Names
  - PowerCentric™



# Azuro, Inc.

- PowerCentric Key Features
  - Complete solution for CTS and post-CTS optimization
- PowerCentric Key Benefits
  - 20% lower power
  - Better clock timing and area
  - Multi-corner, Multi-mode, Multi-voltage

# Incentia



- Company Location
  - Santa Clara, CA, USA
  - Hsinchu, Taiwan
  - Other channels: Japan, India, China, Korea, Israel
- Company Profile
  - Leading technology in timing analysis, management, debugging, and ECO solutions
- Customer success
  - From world top semiconductor, systems, fabless IC companies to emerging startups
  - Numerous customer tape-outs

# Incentia



- Product
  - TimeCraft
    - Fastest STA; signoff quality, LOCV, multi-thread, SI
  - TimeCraft-SSTA
    - Statistical STA
  - ECOCraft
    - Highly efficient post-layout leakage power & hold time ECO
- Key Benefits
  - Significantly reduce time-to-market cycle
    - Fastest STA: 2-10X faster
    - Much shortened ECO closure
  - Improved ECO QOR result

# GiDEL, Ltd.



- Company Location
  - Or Akiva , Israel
- Company profile
  - GiDEL is a leading company in FPGA Systems providing platforms and tools to improve product quality and cut development time at the project level.
- Product names
  - PROC Wizard
  - PROC Boards
  - PROC HILs



- Product Key Features
  - PROC Wizard
    - Automatically connects between SW and HW
  - PROC Boards (StratixIII)
    - From 35K to 100M gate, scalable FPGA boards/system
  - PROC HILs
    - One click solution for FPGA based HILs with MATLAB
- Key Benefits
  - Extremely cost-effective
    - The latest StratixIII boards have very high cost performance
  - Boards are also available for Embedded Systems

# MunEDA

- **Company Location**
  - MunEDA GmbH, Munich, Germany (Headquarters)
  - MunEDA Inc., Sunnyvale, USA (US-Office)
- **Company Profile**
  - Leading EDA technology for analysis and optimization of yield and performance of analog, mixed-signal and digital designs.
- **Product Names**
  - WiCkeD DFM
  - WiCkeD DFY
  - WiCkeD RSM-Modelling

# MunEDA

- Product Key Features
  - WiCkeD DFM
    - Nominal Circuit Analysis and Performance Optimization
  - WiCkeD DFY
    - Statistical Circuit Analysis and Yield Optimization
  - WiCkeD RSM-Modelling
    - Hierarchical Block & System Yield Analysis and Optimization
- Key Benefits
  - Significantly reduce design time & design effort
    - Finalize design projects in days instead of weeks or months
  - Improving design quality and yield
    - Avoid redesigns/respins and maximize the yield



# Ponte Solutions Inc.



- Ponte Solutions locations:
  - Headquarters: San Jose, California
  - Japan Representative: Cubic Micro, Yokohama
- What Does Ponte Solutions do?

Ponte Solutions develops and markets unique model-based software products (DFM) solutions that analyze, predict, and reduce the impact of process variability during the manufacture and design of semiconductors.
- Key Product: Ponte YA Systems™

# Ponte Solutions Inc.



- YA System Includes Multiple DFM Engines
  - CAA Analysis for Limited Yield Sensitivity
  - CAA + Analysis for Systematic Yield Sensitivity
  - VCE Analysis for Contact Etch Correction
  - EPC Analysis for Poly Etch Correction
  - CMP Analysis for Optimal CMP Fill
  - Litho solution via partners
- Why Use YA System in Your Design?
  - Complete DFM analysis in one package
  - Applicable to all phases of your design efforts
  - Integrated into key design cockpits