

# Jan.22 Emerging Company Guided Tour A

## Tour Guide

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**D2S**

**( Special Committee,**

**EDSFair2009Executive Committee)**

# Jan.22 Emerging Company Guided Tour A

**11:50~12:30**

## **Visited Companies**

- **Sidense Corp.**
- **Rapid Bridge LLC**
- **Certicom**
- **nSys Design Systems Pvt Ltd**

# Sidense Corp.



- Company Location
  - Ottawa, Canada
- Company Profile
  - 標準CMOSロジック（180nm, 130/110nm, 90nm, 65/55nm以降）に対応した、構成変更可、フィールド書き込み可能な、OTP/eMT向け、不揮発性メモリ IP コアを提供
- Product Names
  - 1T-Fuse (patented architecture)
  - SLP IP Core (128 - 256Kbit, hard macro)
  - SiPROM IP Core (8K - 8Mbit, hard macro)

# Sidense Corp.



- Product Key Features

- SLP(180nm) Core / SiPROM Core

- 128bit から 8Mbitまでのビット対応
    - Built-in charge pump
    - HDMI, DRM, Chip ID, トリミング、暗号キーなど向け
    - フューズ、マスクROMなどストレージ代替

- Key Benefits

- >20 years of data retention and reliability
  - Extended operating temperature ; -40C to +125C
  - マスクROMへのマイグレーションなど

# Rapid Bridge LLC



- **Company Location**
  - Headquartered in San Diego, California (USA)
- **Company Profile**
  - A leading provider of complete metal programmable Complex Subsystem IP & ASIC Platforms (both pre-defined & custom) at advanced nano-meter technology nodes
- **Product Names**
  - LiquidIP™
    - (LiquidIO, LiquidMXS, LiquidPHY, LiquidSerDes, LiquidMemory, LiquidCell)
  - LiquidASIC™
    - Bridging the gap between FPGAs and ASICs with breakthrough super-gate-array technology
  - LiquidSoC™
    - Custom Configurable platform

# Rapid Bridge LLC



## Product Key Features:

- Broadest, highest-performing, smallest metal-programmable Intellectual Property (IP) solutions
- Industry leading, fully reconfigurable family of high-density, low-power, pre-defined ASIC platforms
  - Cost-effective and rapidly deployable solution offering the simplicity of Gate Arrays with the performance features of Standard Cells
  - Broad selection of advanced IPs and SerDes: DDR2/DDR3 PHYs, USB2.0 PHYs, etc.

## Benefits: Significantly reduces cost, time to market, and risk

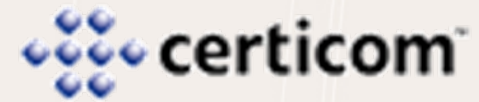
- Reduced unit cost with smallest, highest-performing programmable IP
- Reduced time-to-market: RTL to tested IC in 16 weeks, with low NRE
- Reduced risk with high-performing and fully metal-programmable solutions

# Certicom



- **Company Location**
  - Mississauga, Canada
- **Company Profile**
  - the leading provider of Silicon IP and Security Appliances to protect and manage sensitive semiconductor design and manufacturing information in a global supply chain
- **Product Names**
  - Certicom Asset Management System (AMS)

# AMS Now More Than Ever...



- To reduce development costs
- To reduce operating costs
- To protect against counterfeiting
- To protect against technology transfer
- To create new revenue opportunities
- To gain a competitive edge and increase market share
- To protect corporate branding
- To increase market responsiveness



# nSys Design Systems Pvt. Ltd.



- nSys Location
  - 35463, Dumbarton Court Newark, California, USA
- nSys Profile
  - We leverage the World's largest portfolio of Verification IPs to Accelerate your designs, while lowering your costs and risks.
- nVS (Verification IPs)
  - Key: PCI Express, USB 3.0, Ethernet, SATA, SAS, AXI
  - Others: USB 2.0, SDIO, DDR3, UART, I2C, SPI 4.2

# nSys Design Systems Pvt. Ltd.



- Key Features
  - Verification IPs
    - Option for SystemVerilog (OVM/VMM) & Verilog
  - Verification Services
    - Domain as well as Verification Methodology expertise: SystemVerilog, e, SystemC & Vera
- Key Benefits
  - Verification IPs
    - Flexibility of licensing options for Unlimited / Source code licenses
  - Verification Services
    - Proven track record of Accelerating designs