



Emerging Company Guided Tour (A)

14:15-15:15

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Academic Research Center

Electronic Design and Solution Fair 2010

Webにない“新しい”がここにある。
“New”@EDSFair, on-site, Not on web-site

Emerging Company Guided Tour A

14:15-15:15, Jan. 28

Visited Companies

- Synfora, Inc.
- REAL INTENT, INC.
- CLIOSOFT, INC.
- Arasan Chip Systems, Inc.
- IPextreme Inc. Constellations

Synfora, Inc.



• Company Location

- 本社： 米国カリフォルニア州、マウンテン・ビュー市
- 国内： 神奈川県横浜市（新横浜）

• Company Profile

- ヒューレット・パカード(HP)社の研究所をスピン・アウト
- C/C++からVerilog RTLへの高位合成ツールを開発

• Product Name

- PICO Extreme (SOC向け合成)
- PICO Extreme Power (パワー最適化を含むSOC向け合成)
- PICO Extreme FPGA (FPGA向け合成)

Synfora, Inc.

- Product Key Features
 - C/C++記述の大規模かつ複雑なアルゴリズムをSOC/FPGA向けの最適なRTL (Verilog) に変換
 - 自動処理でマルチスレッドを実現するコンパイラ技術
 - 独自のTCAB技術でより複雑な階層設計をサポート
- Key Benefits
 - 大規模回路の一括合成が可能のため、性能、エリア、パワーのトレードオフを高いレベルで検討可能
 - ブロック単位で処理する合成ツールでは難しいマルチ階層の並列化を自動処理
 - System Cツールで要求されるブロック間インタフェースの明示的な記述は不要

Real Intent Inc.

- Real Intent Locations
 - Headquarter: Sunnyvale, California, USA
 - Real Intent KK: Tokyo, Japan
 - Worldwide distributors – Europe, Israel
- Real Intent Profile
 - Industry leader in automating formal techniques for design verification
- Real Intent Product Families
 - Ascent
 - Meridian
 - PureTime

Real Intent Inc.

- Product Key Features
 - Ascent
 - Complete solution for early functional verification
 - Meridian
 - Comprehensive clock domain crossing verification
 - PureTime
 - Constraints validation with exception verification
- Key Benefits
 - Automatic formal technology catches bugs easily
 - Improve design quality
 - No need for testbench development
 - Reduce verification and project cycle

ClioSoft, Inc.



- Company Location
 - 本社:フレ蒙特(Fremont), 米国
 - 日本国内代理店:株式会社シンコム(www.synkom.co.jp)
- Company Profile
 - デザインデータのマネジメント
- Product Name
 - SOS
 - SOS viaDFII(Cadence Virtuoso flow)
 - SOS viaLaker(SpringSoft Laker flow)
 - SOS viaCustomDesigner(Synopsys Customer Designer flow)

ClioSoft, Inc.



• Product Key Features

- 排他制御により、同一データを複数のユーザーの同時変更を防ぐ
- 過去の任意のバージョンの差分と履歴を確認できる
 - SchematicとLayout回路の変更をGUIでハイライト可能

• Key Benefits

- キャッシュとシンボリックリンク機能により、ディスク容量の最適化が可能
- EDAツールへのインテグレーションにより、設計環境の変更が不要

Arasan Chip Systems



- Company Location
 - San Jose California, USA
- Company Profile
 - Total IP solution provider of IP, driver stack, VIP, Verification kit and service .
 - 160 engineers
- Product Name
 - SD/SDIO3.0 IP Total Solution
 - MIPI IP Total Solution
 - Multi-Card-Reader Solution

Arasan Chip Systems



- Product Key Features
 - SD/SDIO3.0 IP Total Solution
 - Supports UHS-I operation and 2TB memory now!
 - MIPI IP Total Solution
 - Supports all components of MIPI (DSI, CSI, D-PHY etc)
 - Multi-Card-Reader Total Solution
 - 75-in-1 Host IP Controller
- Key Benefits
 - Easier to implement of IP
 - Provide IP, Driver, VIP, Hard verification Kit and Service& support

Constellations by IPextreme

- Profile
 - Constellations is a program to help medium and small size semiconductor IP companies find new sales opportunities for their products
- Product Names
 - Semiconductor IP for ASIC and FPGA
 - Design Services
 - IP Repository

Constellations by IPextreme

- Product Key Features
 - Sales leads from design projects that need your IP
 - Leverage the sales forces of other top IP companies
 - Worldwide joint marketing opportunities with other top IP companies
- Key Benefits
 - More sales opportunities for your IP products
 - Lowers your cost of sales and marketing