Emerging Company Guided Tour　B

13:00-14:00
Toshiyasu Akiyama
ASKAssist

Electronic Design and Solution Fair 2010

Webにない"新しい"がここにある。
"New"@EDSFair, on-site, Not on web-site

Emerging Company Guided Tour C

13:00-14:00, Jan. 29

Visited Companies

- **POLYTEDA SOFTWARE CORPORATION**
- **MICROLOGIC DESIGN AUTOMATION INC.**
- **Dorado Design Automation, Inc.**
- **TEKLATECH A/S**

POLYTEDA Software Corp.

- Company Location
  - Toronto, Canada

- POLYTEDA is a rapidly growing EDA company, focused on delivering fast, accurate and affordable verification solutions.

- Product Name
  - PowerDRC/LVS

POLYTEDA Software Corp.

- Improving Design Productivity
  - PowerDRC/LVS is consistently 4 to 6X faster than the market leader for DRC processing. Performance delta varies with design size.

- Schedule Predictability
  - Performance is predictable across process nodes and design styles- not "Design Dependant"

- Imperative technology for
  - Large SOC designs, Projects which require excellent flat capacity and performance

Micrologic

- ## Company Location
  - San Jose, California, USA

- ## Company Profile
  - The leading provider of interactive DRC and reliability checkers, making the design signoff ready faster

- ## Product Names
  - VisualDRC
  - NanoRVInteractive

Micrologic

- **Product Key Features**
  - VisualDRC
    - Interactive DRC checks of signoff rule deck
  - NanoRVInteractive
    - Interactive Reliability checks (EM, Antenna/Fuse effects)

- **Key Benefits**
  - Cut layout schedules by up to 50%
  - Deals with conditional, connectivity rules, etc.
  - Start reliability checks much earlier
  - Significantly increase end product quality

Dorado

- Company Location
  - HsinChu, Taiwan

- Company Profile
  - Provides complete ECO solutions to IC industry

- Product Names
  - Tweaker-F1:   For Functional ECO
  - Tweaker-T1:   For Timing ECO
  - Tweaker-M1: For Metal ECO
  - Tweaker-P1:   For Power ECO

Dorado

- Tweaker Key Features
  - Tweaker F1 + M1
    - Timing driven Functional ECO and Post-Mask ECO
  - Tweaker T1 + P1
    - Multi-Mode, Multi-Corner Timing ECO and Power ECO

- Tweaker Key Benefits
  - 3 ~ 10x Faster ECO
  - 30%↑ less area overhead for Hold Time ECO
  - 15% ~ 25% less leakage power

Teklatech

- ## Company Locations
  - Copenhagen Denmark, Santa Clara USA, Tokyo Japan

- ## Company Profile
  - Teklatech develops EDA tools for dynamic power optimisation on digital and mixed-signal IC's to reduce IR Drop, EMI and digital noise.

- ## Product Names
  - FloorDirector® plus options: Early Rail Analysis, Dynamic Power Shaping and Frequency Domain Analysis

Teklatech

- **Product Key Features**
  - FloorDirector reduces Dynamic Power on netlist level
    - <u>No</u> add/change of digital logic.<u>No</u> performance reductions.
  - Attacks root cause of IR Drop, EMI and noise problems
    - Optimisations are <u>additional</u> to existing on-chip methods

- **Key Benefits**
  - Improvements increasing backend design success
    - Reduce ECO, higher efficiency -> Time to Market -> $$$
  - Reducing direct and associated IC and program costs
    - Less mask sets and wafer spins -> $$$