



Emerging Company Guided Tour (C)

14:15-15:15

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ASKAssist

Electronic Design and Solution Fair 2010

Webにない“新しい”がここにある。
“New”@EDSFair, on-site, Not on web-site

Emerging Company Guided Tour C

14:15-15:15, Jan. 29

Visited Companies

- EDXACT
- Concept Engineering GmbH
- Physware Inc.
- Interoperable PDK Alliance

EDXACT



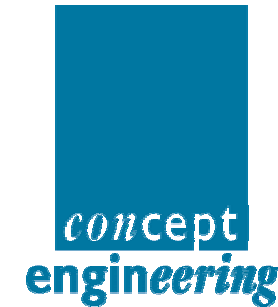
- Company Location
 - Grenoble, France
- Company Profile
 - EDXACT is an EDA leading provider of advanced technologies to analyze parasitic effects and accelerate circuit simulators
 - Partnership with Major vendors
- Product Names
 - Jivaro : Model Order Reduction For Parasitics
 - Comanche : Parasitics Explorer and Analyser (ESD, EM, ..)

EDXACT



- Product Key Features
 - Jivaro
 - Accurate Model Order Reduction, Reduces all types of parasitics massively & selectively, Speeds up Post Layout analyses, including E.M. flows
 - Comanche
 - Pins to pins resistances, Delays, SYZ parameters X coupling Cs, Resistance path analysis, etc ..
- Key Benefits
 - Accelerate verification time, gain productivity
 - Your verification is more accurate, better yield

Concept Engineering GmbH



- Company Location
 - Freiburg, Germany
- Company Profile
 - Concept's customizable debugging tools provide design engineers with the ability to quickly understand, debug and optimize their designs
- Product Names
 - SpiceVision PRO
 - GateVision PRO
 - RTLvision PRO

Concept Engineering GmbH

- Product Key Features
 - SpiceVision PRO
 - Transistor-level debugging (HSPICE, CDL, CALIBRE, ...)
 - GateVision PRO
 - Gate-level debugging (Verilog, EDIF, LEF/DEF, SDF)
 - RTLvision PRO
 - RTL-level debugging (VHDL, Verilog, SystemVerilog)
- Key Benefits
 - Supports pre- and post-layout transistor-level debugging (SpiceVision PRO)
 - Helps engineers to understand chip designs and to integrate IP blocks (GateVision PRO, RTLvision PRO)

Physware Inc.

- Company Location
 - Bellevue Washington, USA
- Company Profile
 - The leading provider of high-speed innovative 3D electromagnetic solutions that enable broadband verification and design across the chip-package-board ecosystems in wireless, memory, FPGA, microprocessor, analog-RF applications
- Product Names
 - PhysWAVE
 - PhysAPEX
 - PhysBAND

Physware Inc.

- Product Key Features
 - PhysWAVE
 - 3D Fullwave Signal Integrity, Power Integrity, EMI
 - PhysAPEX
 - 3D Accelerated Package Extraction
 - PhysBAND
 - Fast Full-wave Modeling for System-level SSO,PI,SI
- Key Benefits
 - True-3D broadband package-board solution
 - Maxwell accurate boundary element accelerated method
 - 10x-100x speed and capacity advantage
 - Fast linear scaling, multi-threaded solver technology

Interoperable PDK Alliance

- Company Location
 - Based in Silicon Valley, US with > 20 global members
- Company Profile
 - An alliance of companies whose goal is to create and promote standards for **Interoperable Process Design Kits** (PDKs) and flows
- Product Names
 - Interoperable 90nm PDK and reference circuit
 - Interoperable PyCells™ (Python PCells)
 - Interoperable Tcl call backs and iCDF standard

Interoperable PDK Alliance

- Product Key Features
 - Interoperable OpenAccess™ based PDKs
 - Used by TSMC in their iPDK
 - PCells with less code, faster execution and portability
 - ROD-like APIs without the performance penalty
- Key Benefits
 - Reduce PDK development and support costs
 - Create choices in building your custom design flow
 - Increase design reuse and portability